

FIG. 1

32

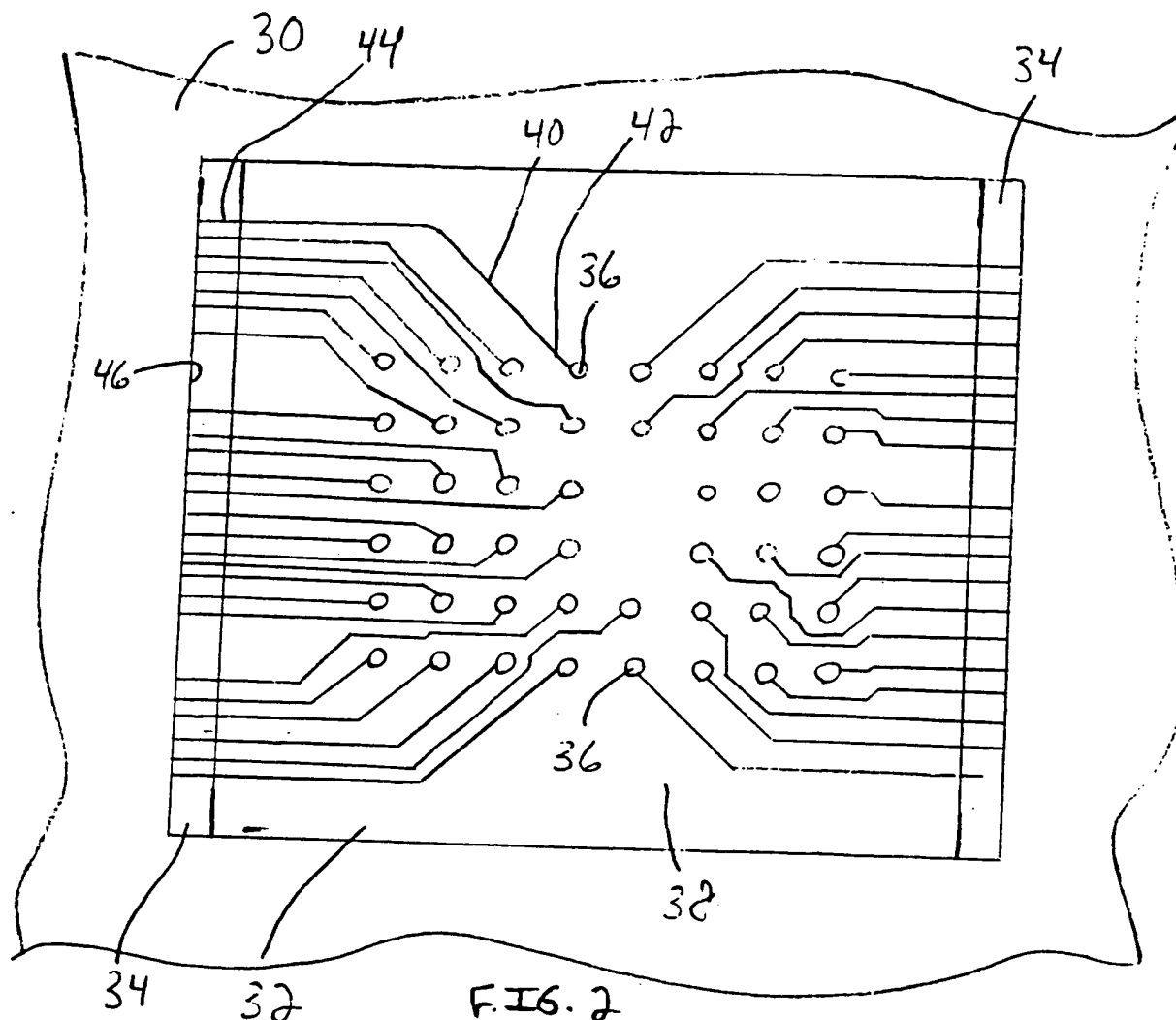
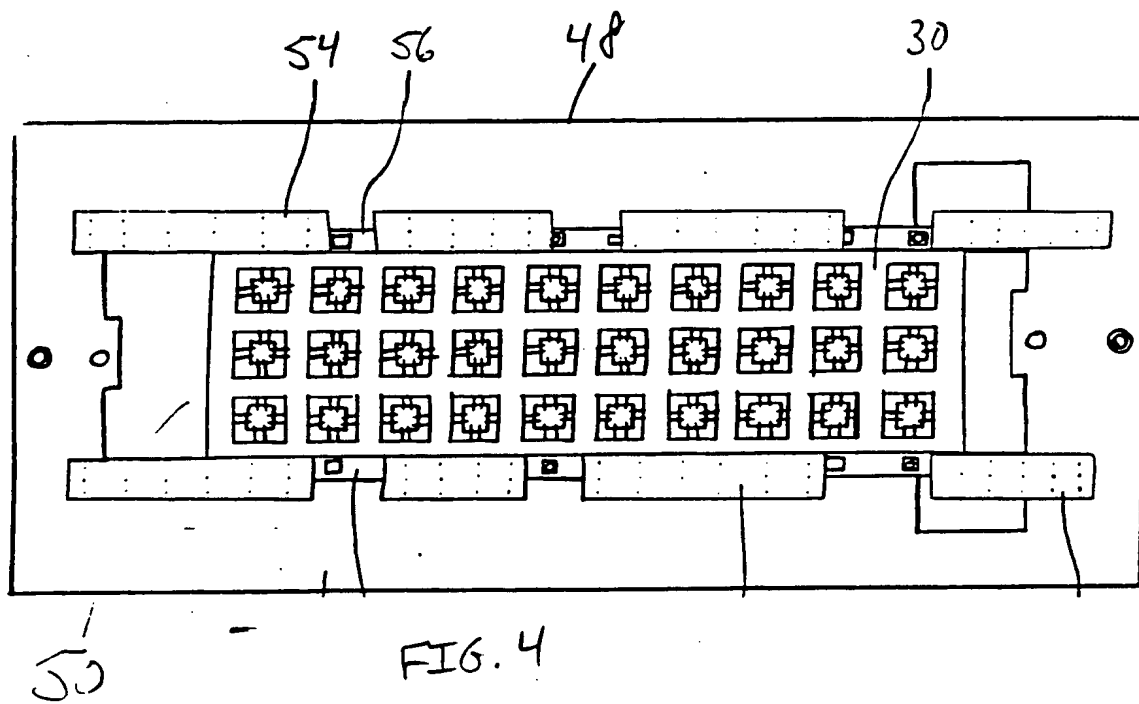
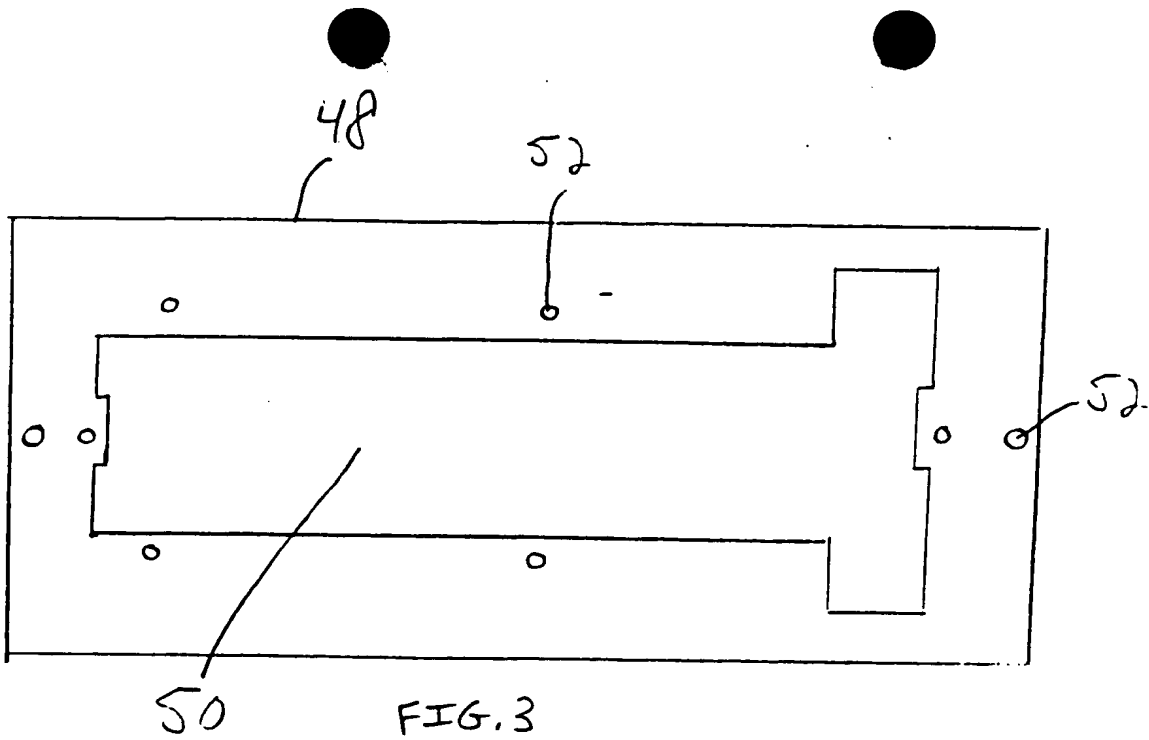


FIG. 2



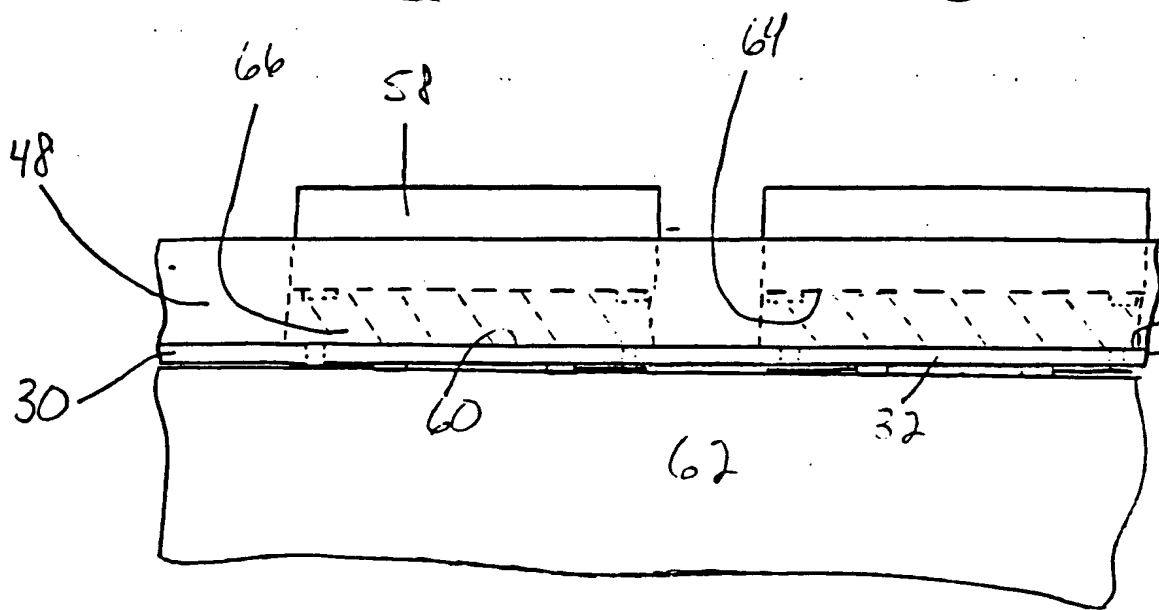


FIG. 5A

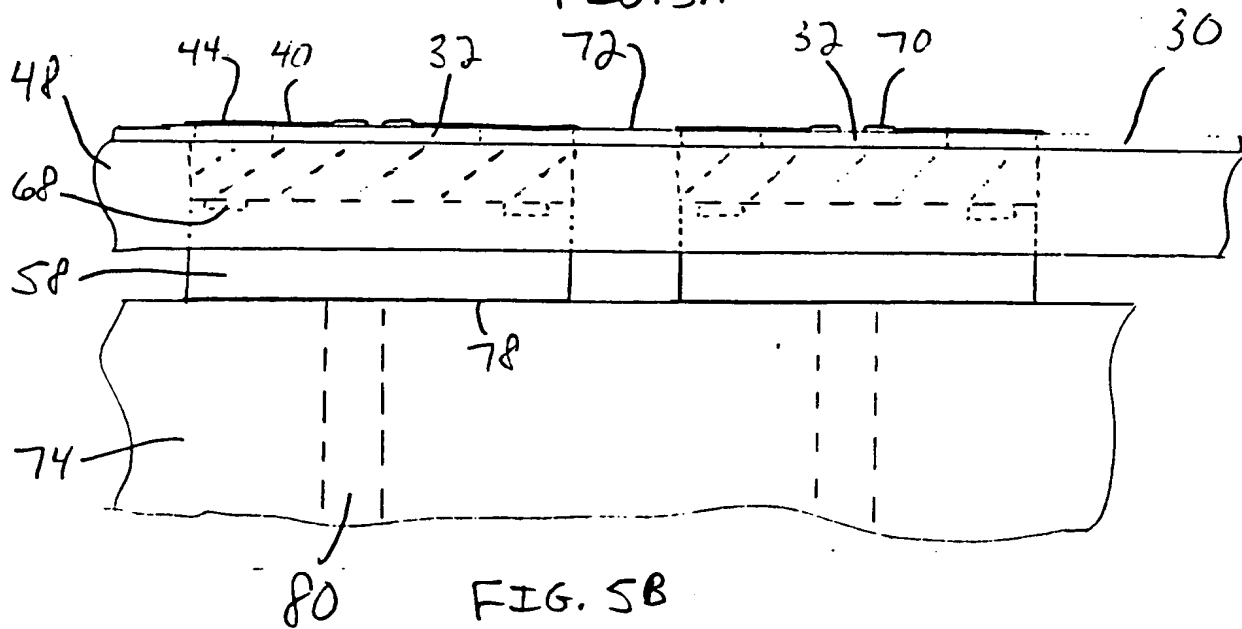


FIG. 5B

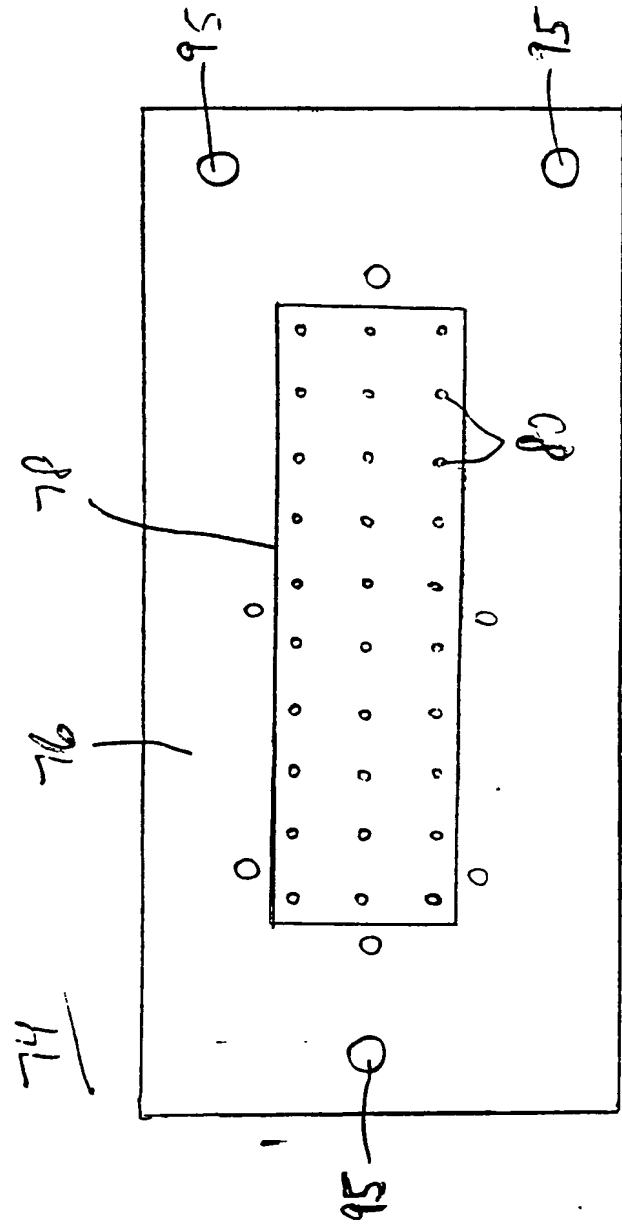


FIG 6A

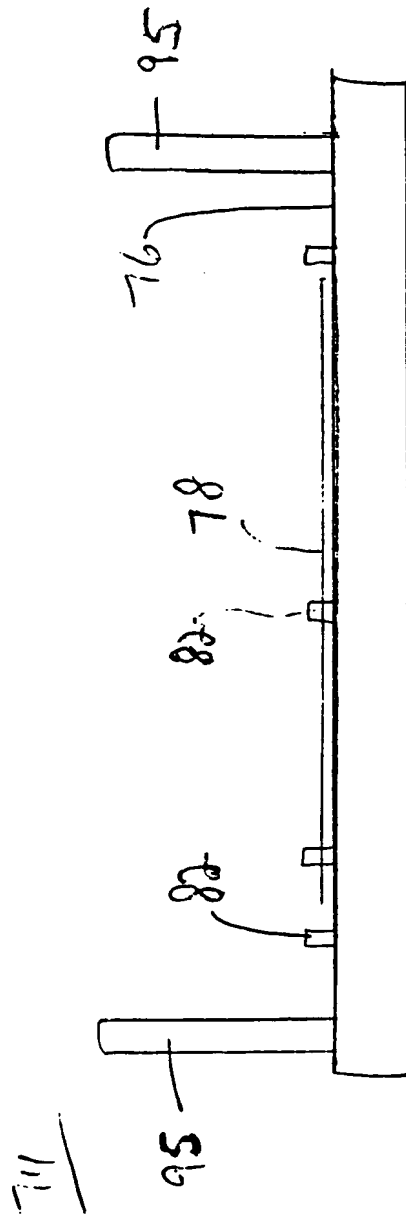


FIG 6B

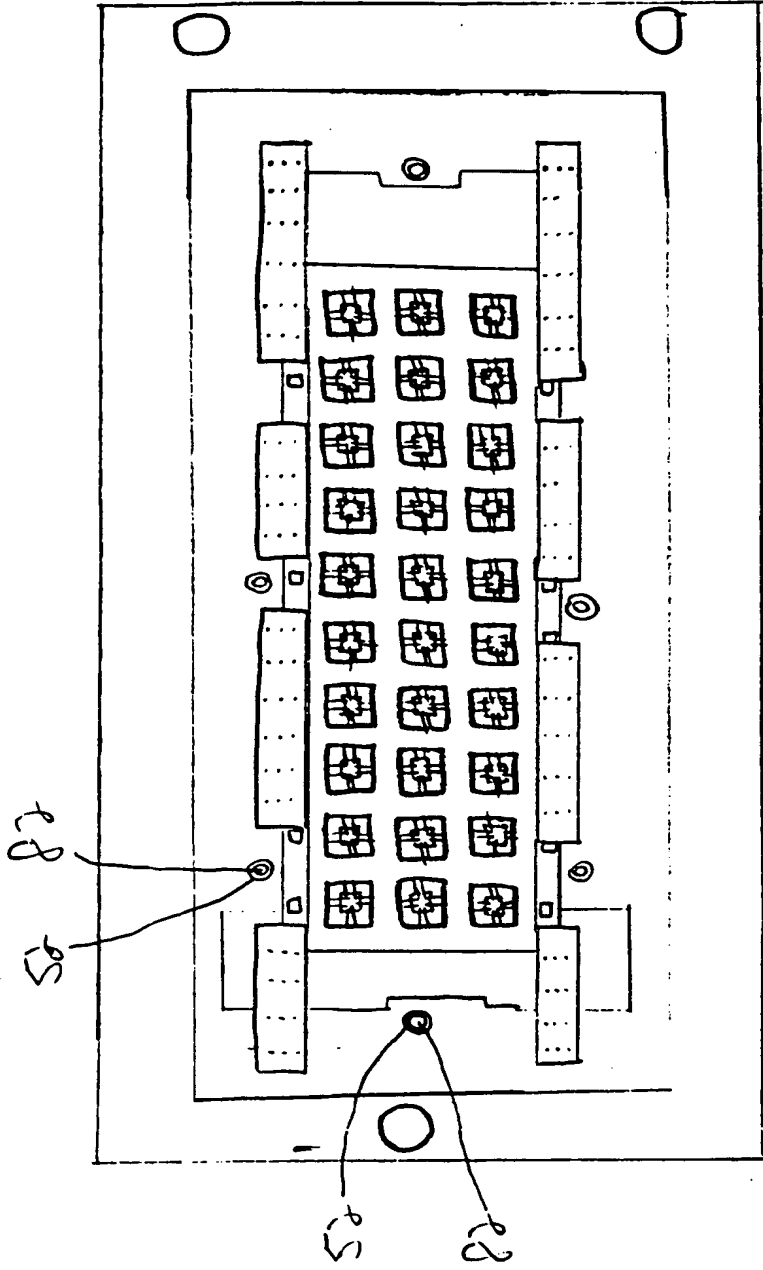


FIG. 7A

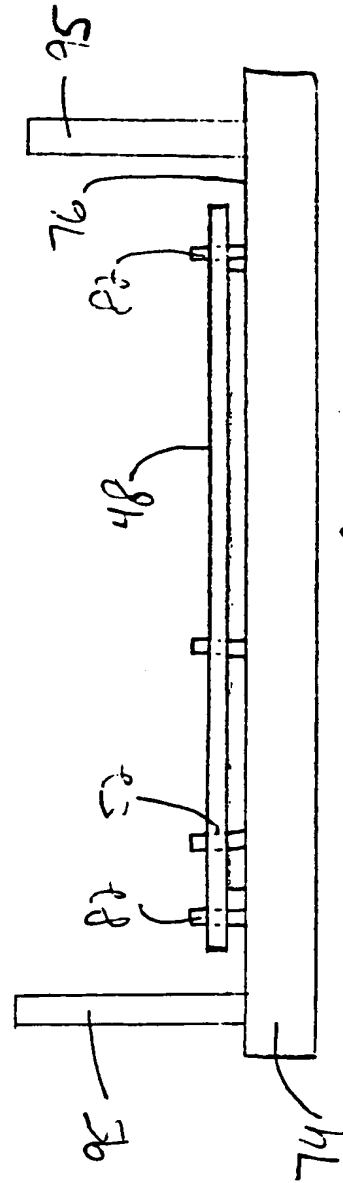


FIG. 7B

22

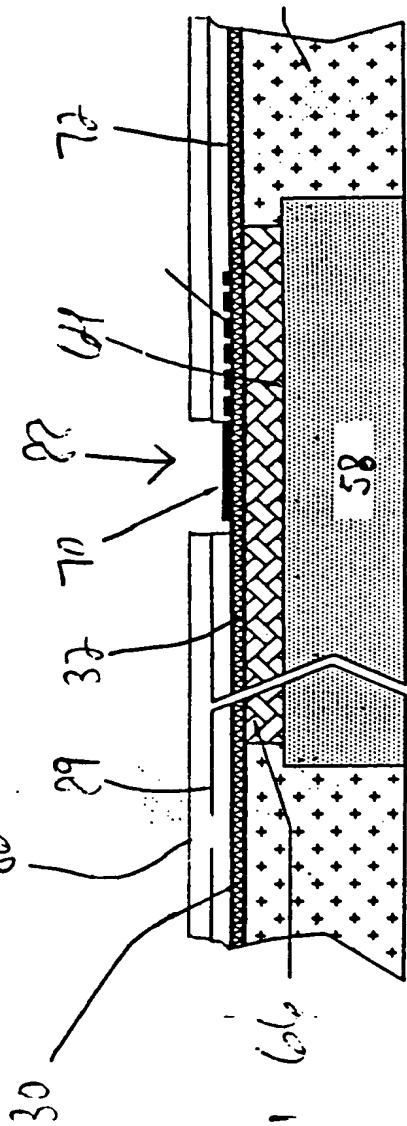


FIG. 8

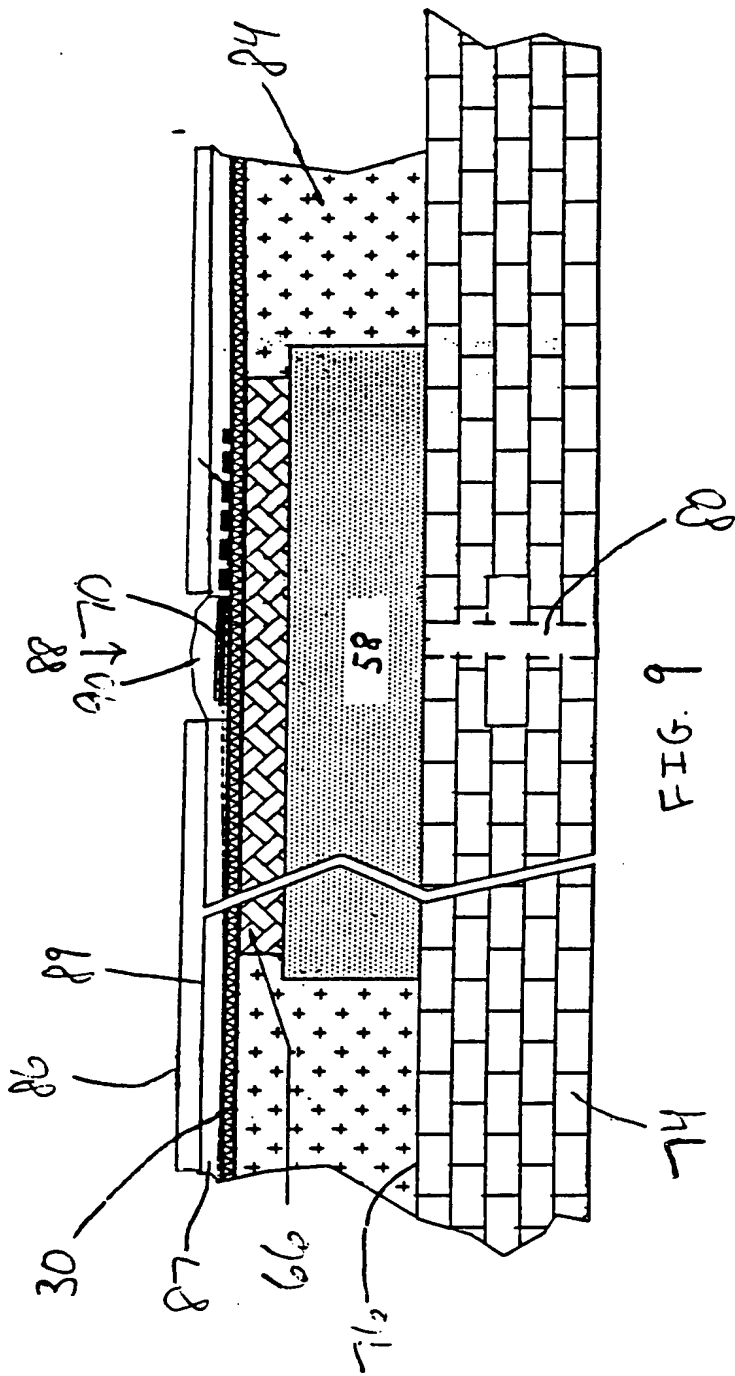


FIG. 9

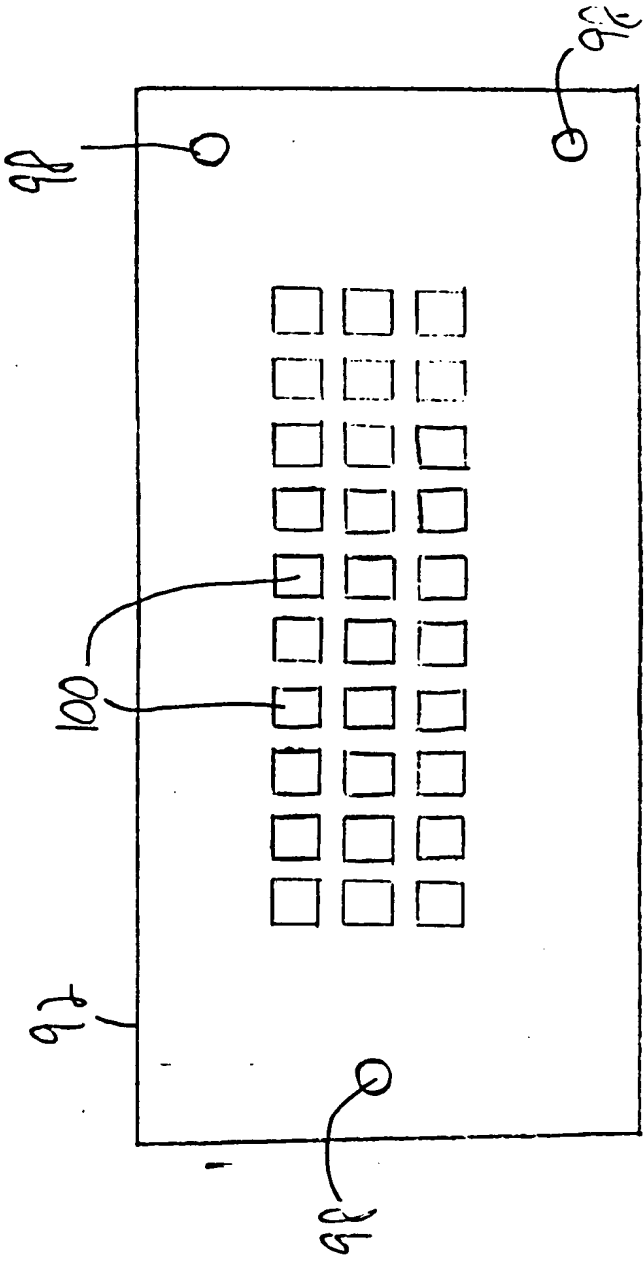


FIG. 10A

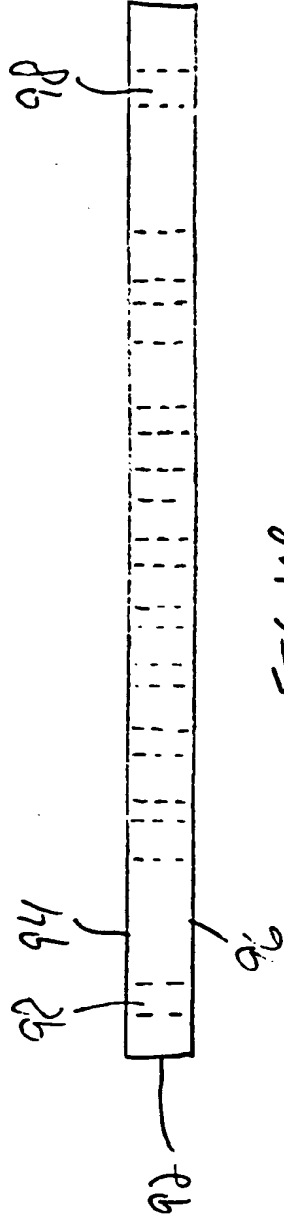
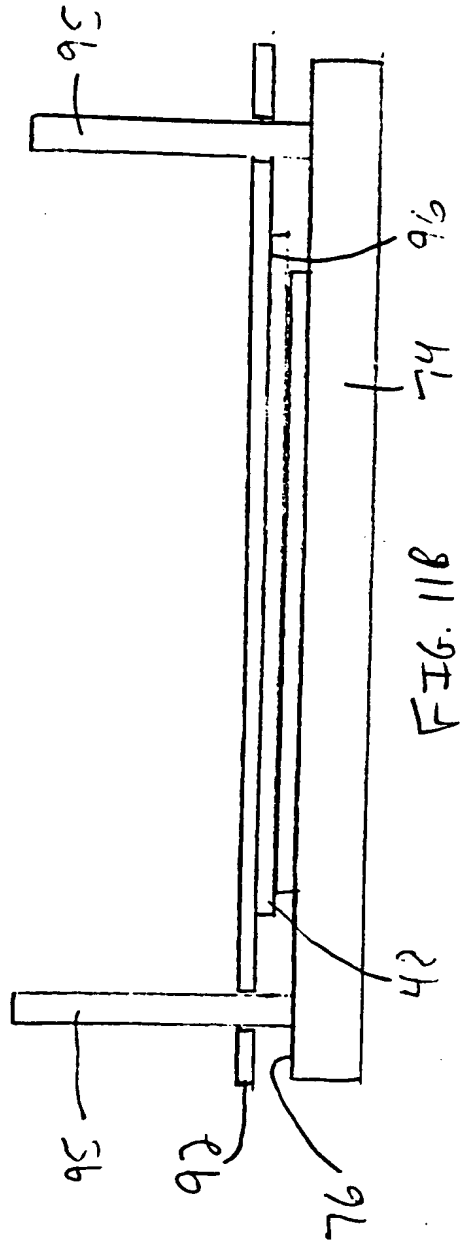
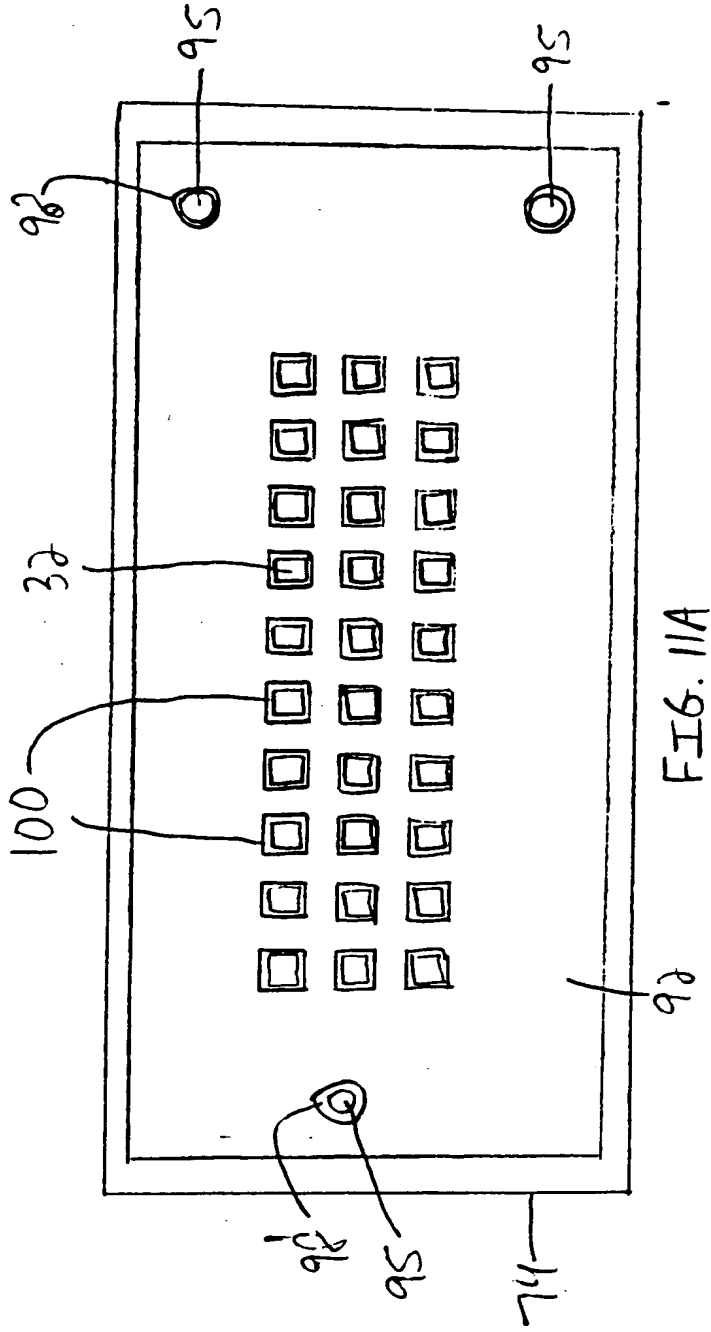


FIG. 10B

FIG. 11A is a top view of the device 100 showing a rectangular frame 74 with a central array of 32 square elements 32. The frame 74 is defined by a border 92. The array 32 is arranged in a 4x8 grid. The frame 74 has four corner fasteners 95. The array 32 is connected to a central line 100.





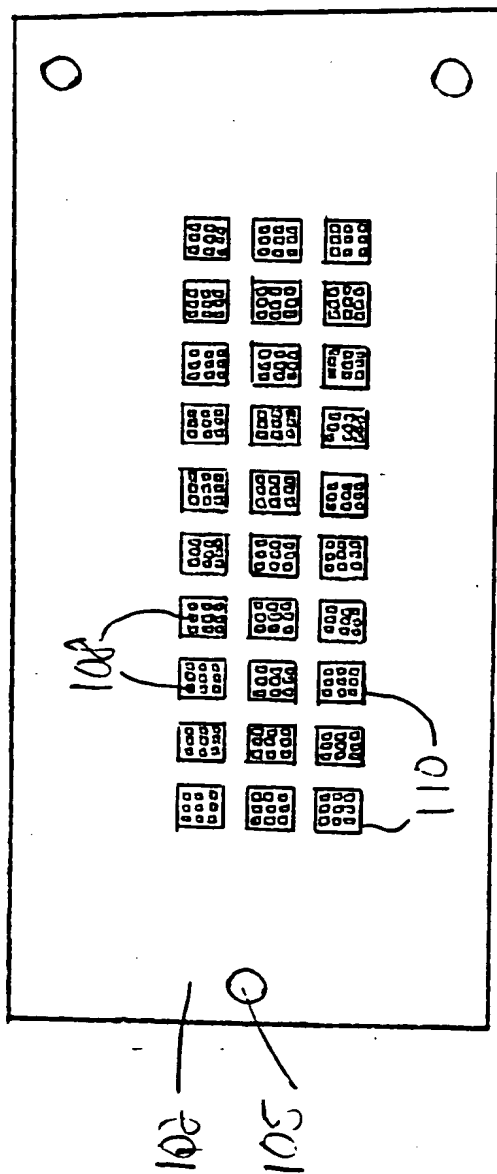


FIG. 12A.

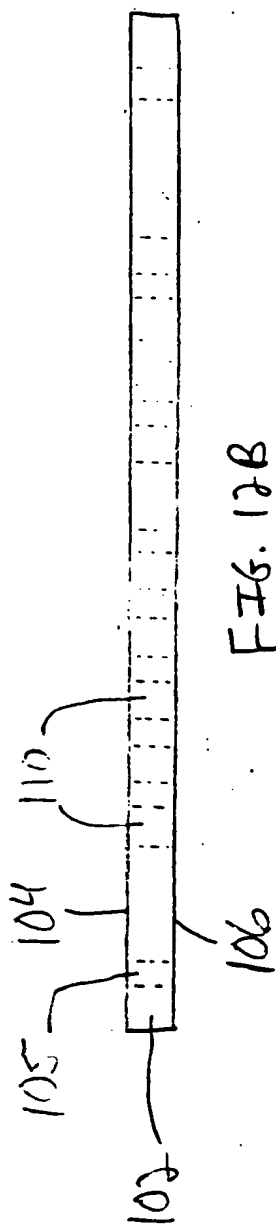


FIG. 12B



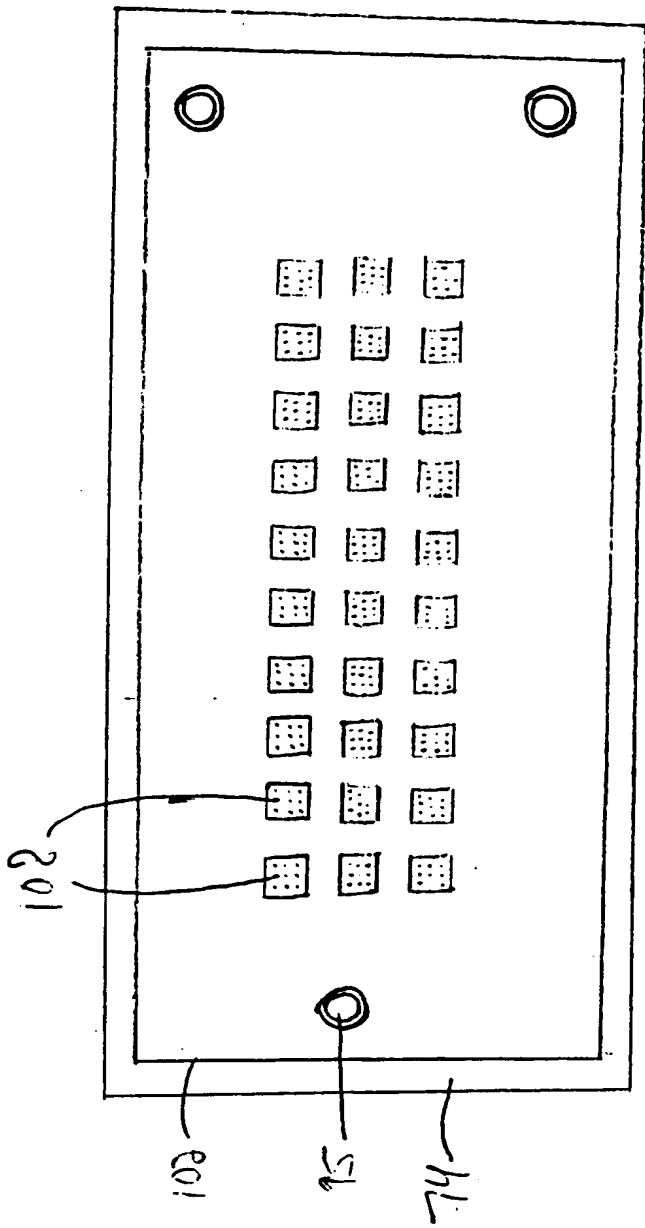


FIG. 14A

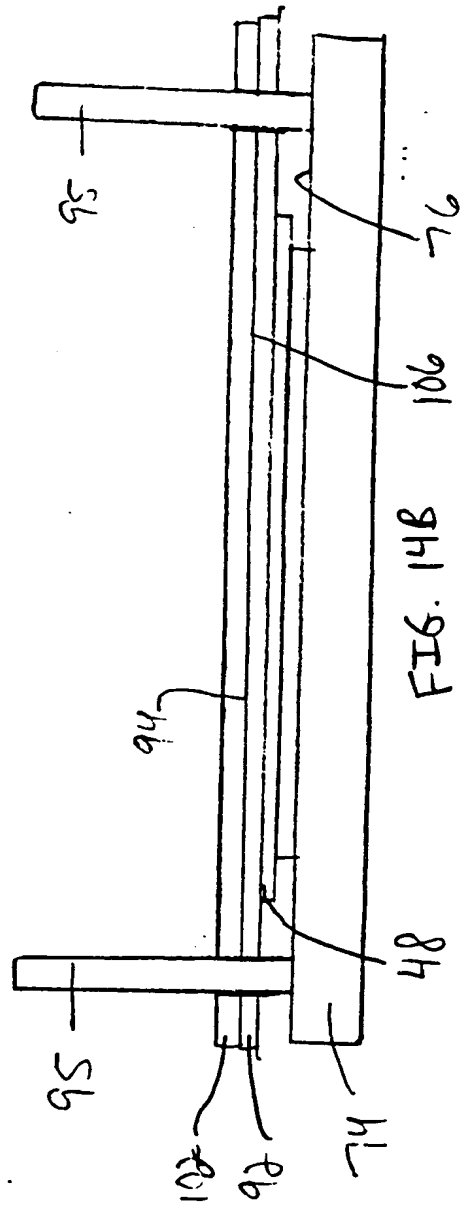


FIG. 14B

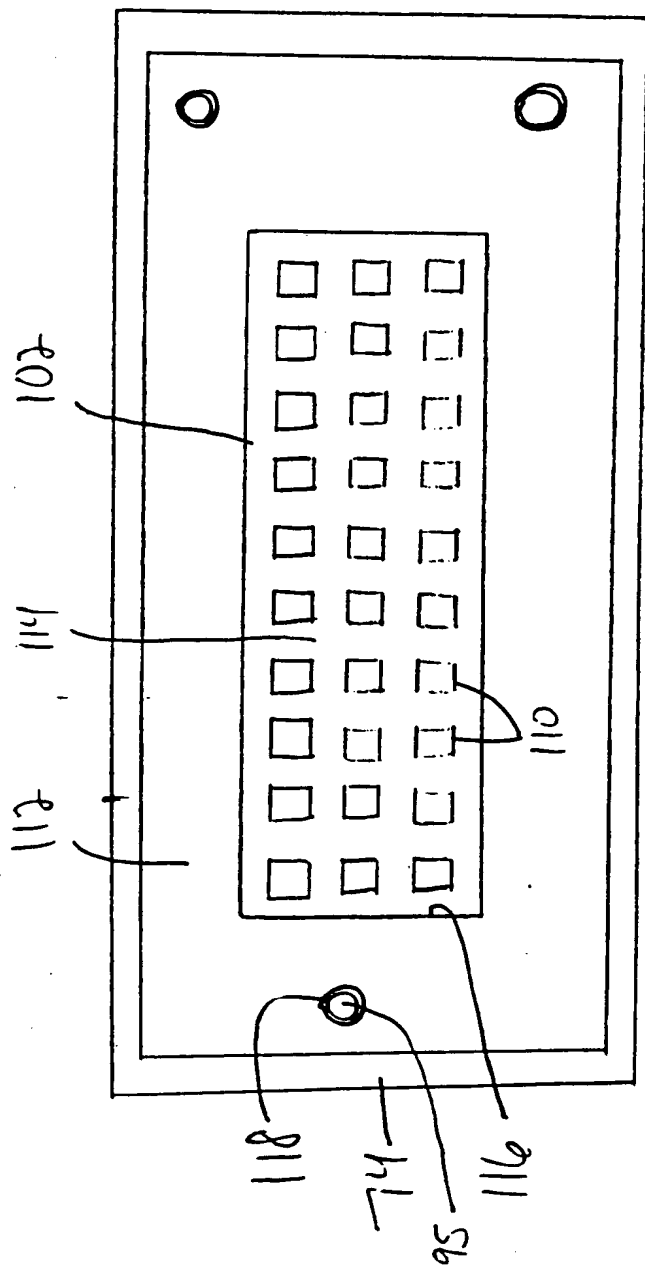


FIG. 15A

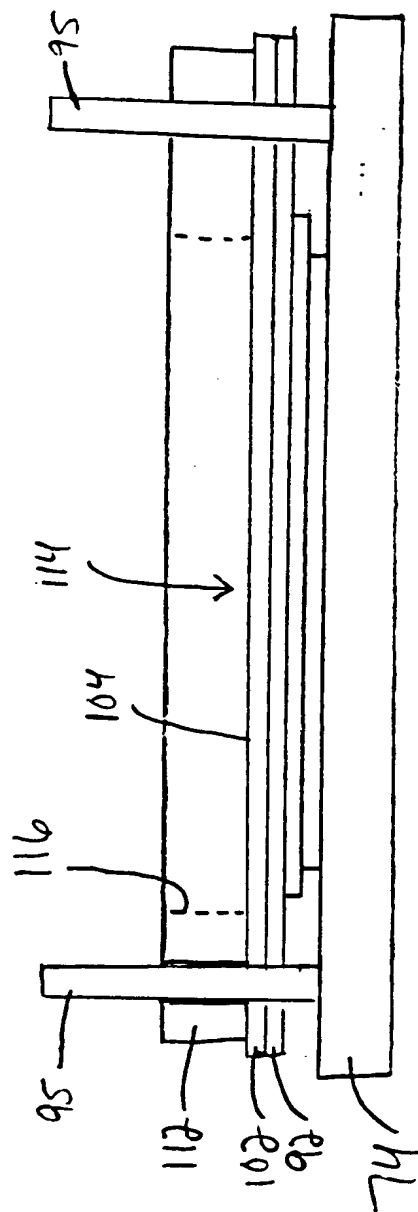
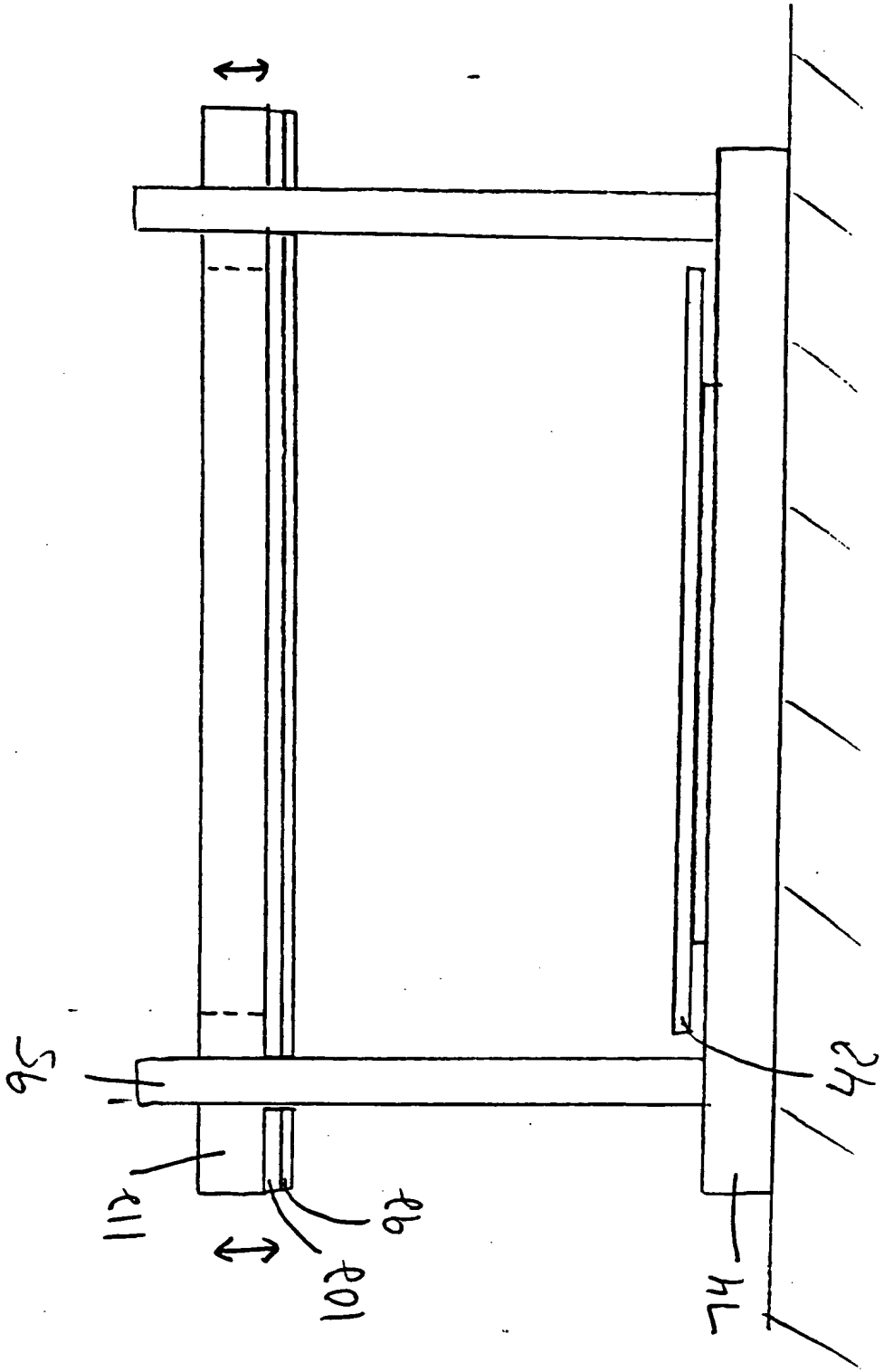


FIG. 15B

FIG. 16



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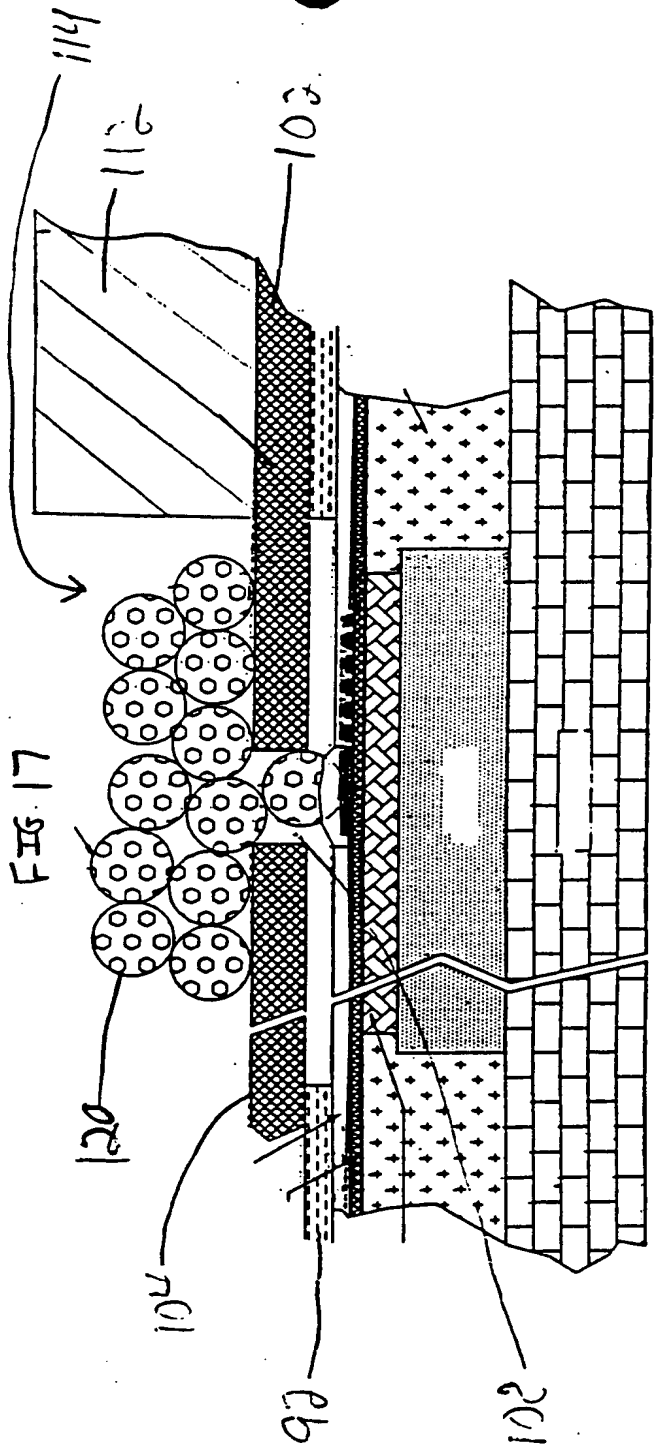
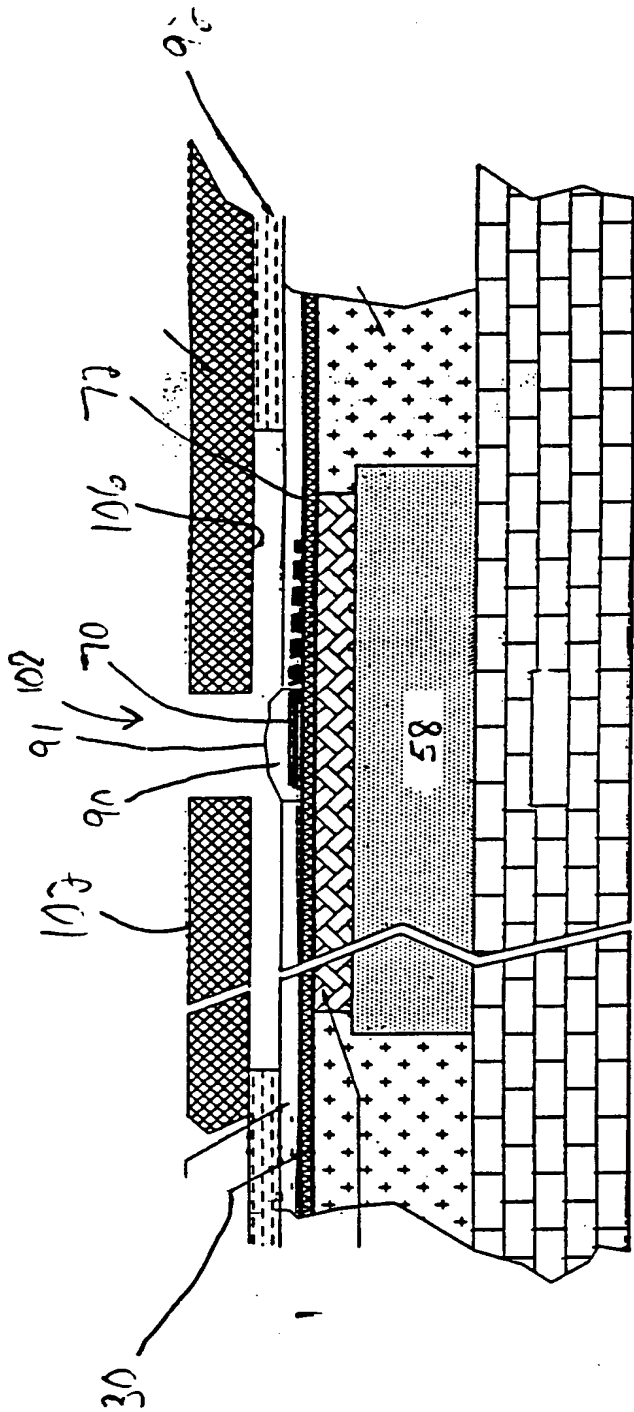
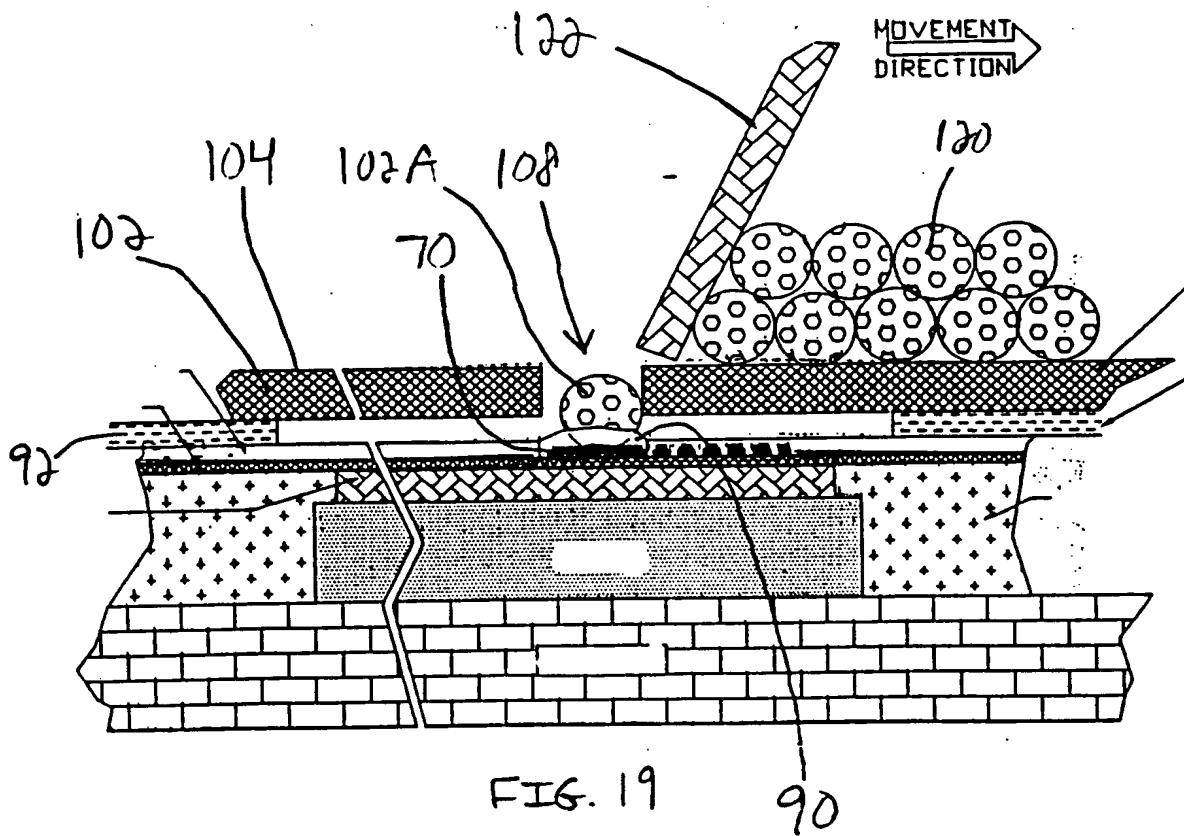


FIG. 18



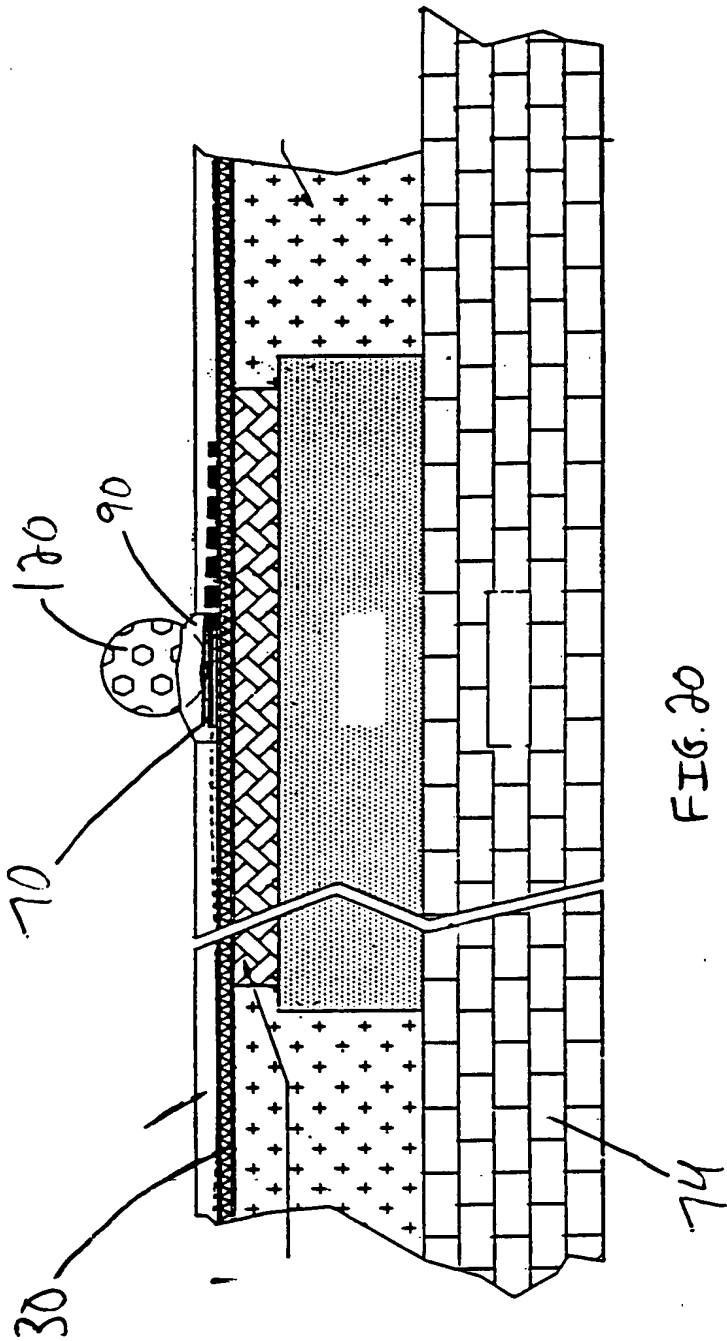


FIG. 20

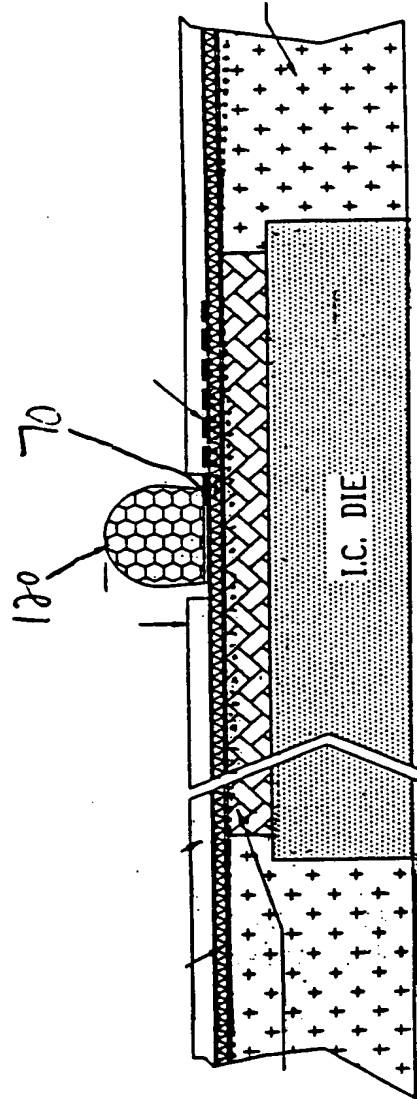


FIG. 21



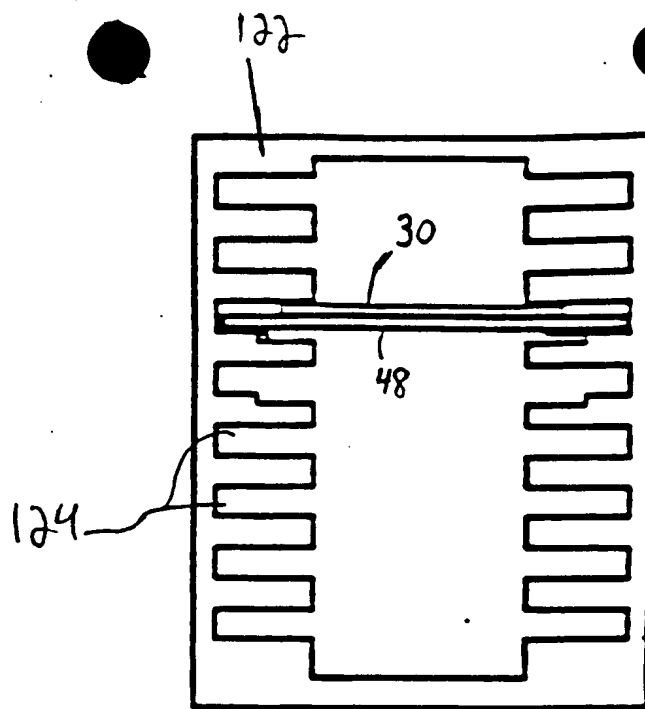


FIG. 22

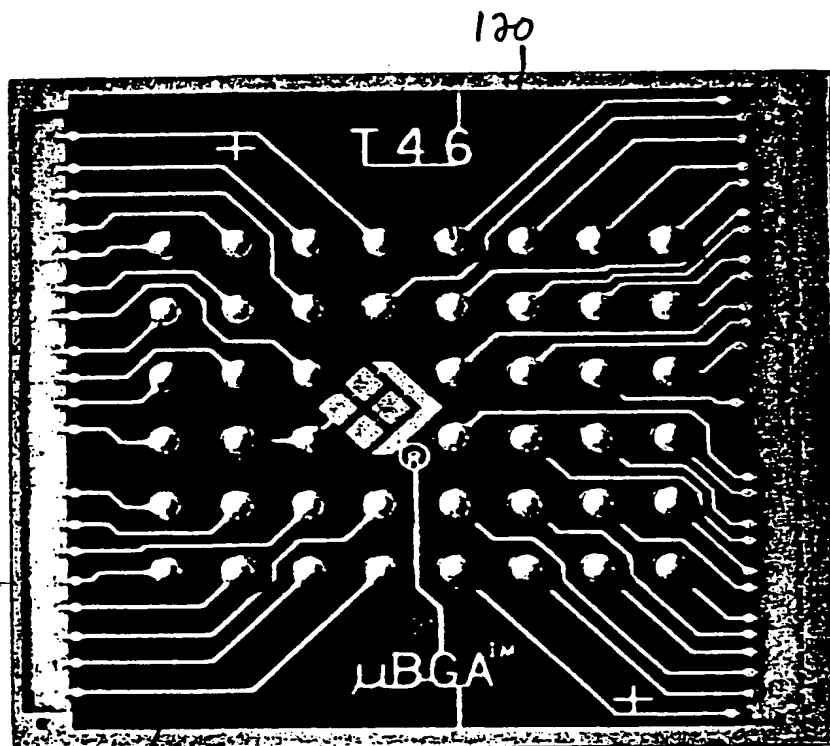


FIG. 23

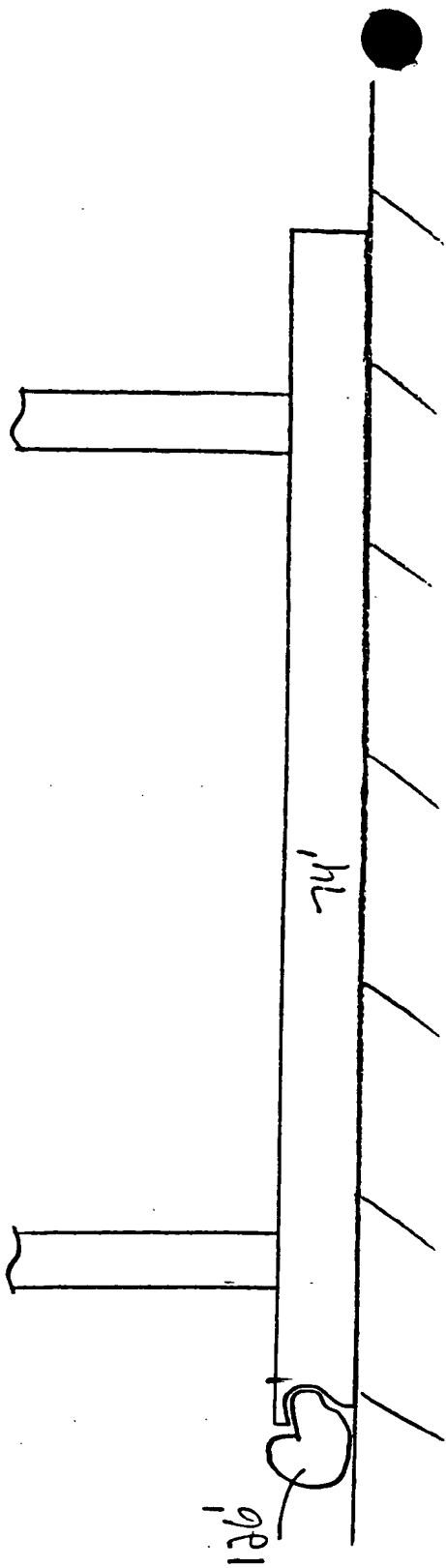


FIG. 24A

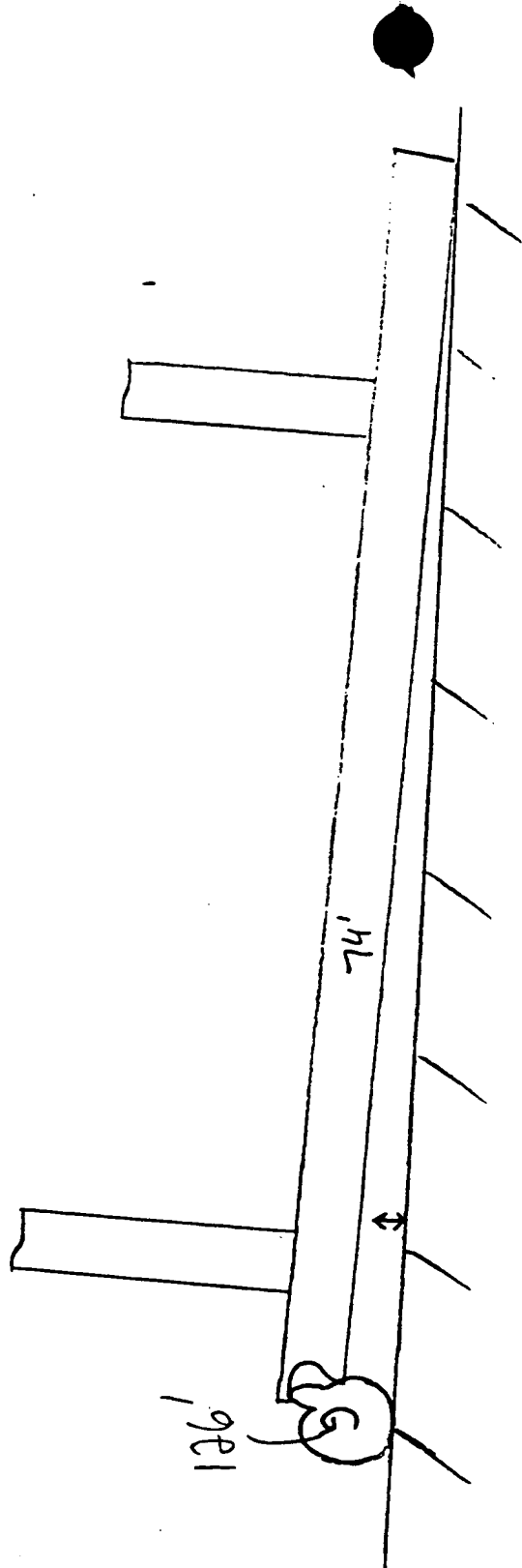


FIG. 24B

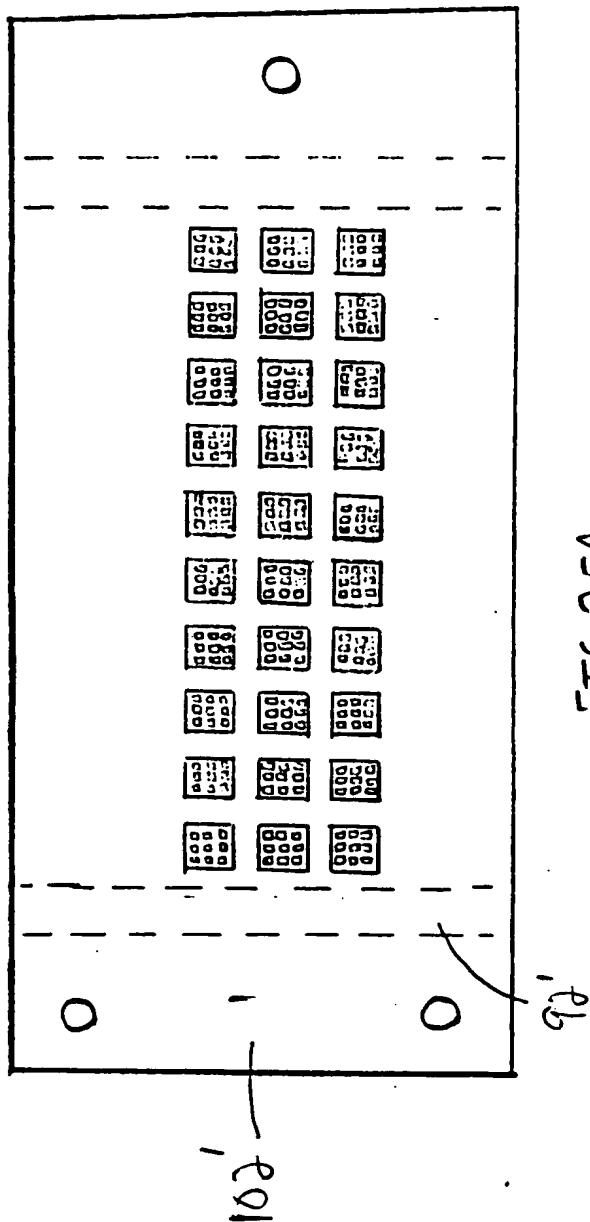


FIG. 25A

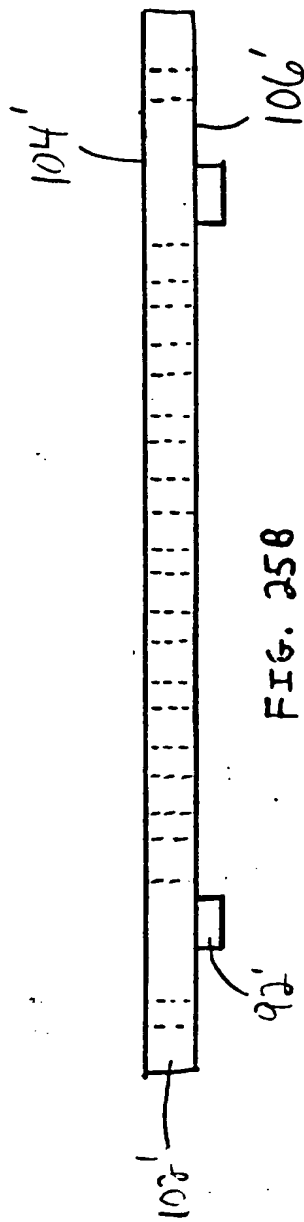


FIG. 25B